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## Inventor Information for 09/745743

<b>Inventor Name</b>	<b>City</b>	<b>State/Country</b>
WASHBURN, JAMES G.	PALO ALTO	CALIFORNIA

<b>Appln Info</b>	<b>Contents</b>	<b>Petition Info</b>	<b>Atty/Agent Info</b>	<b>Continuity Data</b>	<b>Foreign Data</b>
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## Inventor Name Search Result

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Last Name = WASHBURN

First Name = JAMES

Application#	Patent#	Status	Date Filed	Title	Inventor Name 21
<u>60470421</u>	Not Issued	159	05/15/2003	LOW CAPACITANCE BIDIRECTIONAL OVER- VOLTAGE PROTECTION THYRISTOR DEVICE FOR TELECOMMUNICATIONS	WASHBURN, JAMES R.
<u>60409568</u>	Not Issued	159	09/11/2002	LOW CAPACITANCE - SUB SV ESD CLAMPING DEVICE FOR HIGH SPEED DATA TRANSMISSION LINE PROTECTION	WASHBURN, JAMES
<u>60371496</u>	Not Issued	159	04/11/2002	LOW CAPACITANCE BIDIRECTIONAL OVER- VOLTAGE PROTECTION THYRISTOR DEVICE FOR TELECOMMUNICATIONS	WASHBURN, JAMES R.
<u>60273725</u>	Not Issued	159	03/07/2001	INTERRUPTABLE HIGH- VOLTAGE CURRENT LIMITER SUITABLE FOR MONOLITHIC INTEGRATION	WASHBURN, JAMES R.
<u>10860016</u>	Not Issued	020	06/04/2004	CASCADED LOAD BALANCING	WASHBURN, JAMES
<u>10298606</u>	Not Issued	030	11/19/2002	PRECOMPUTE LOGIC FOR SOFTWARE PACKET PROCESSING	WASHBURN, JAMES
<u>10298552</u>	Not Issued	030	11/19/2002	HIERARCHICAL POLICERS FOR ENFORCING DIFFERENTIATED TRAFFIC BEHAVIOR	WASHBURN, JAMES
<u>10098493</u>	Not Issued	030	03/18/2002	HIGH PERFORMANCE PROBABILISTIC RATE POLICER	WASHBURN, JAMES
<u>10090535</u>	Not Issued	161	03/05/2002	INTERRUPTABLE HIGH- VOLTAGE CURRENT LIMITER SUITABLE FOR MONOLITHIC	WASHBURN, JAMES RAY

				INTEGRATION	
<u>09745743</u>	Not Issued	030	12/26/2000	PROBABILISTIC COUNTER	WASHBURN, JAMES G.
<u>09436055</u>	<u>6467019</u>	150	11/08/1999	METHOD FOR MEMORY MANAGEMENT IN TERNARY CONTENT ADDRESSABLE MEMORIES (CAMS).	WASHBURN , JAMES
<u>09388223</u>	<u>6145529</u>	150	09/01/1999	MONITORING STEAM TRAPS USING RF SIGNALING	WASHBURN , JAMES F.
<u>09287302</u>	<u>6639915</u>	150	04/07/1999	METHOD AND APPARATUS FOR TRANSMISSION OF VOICE DATA IN A NETWORK STRUCTURE	WASHBURN , JAMES
<u>09187285</u>	<u>6266262</u>	150	11/05/1998	ENHANCED BINARY CONTENT ADDRESSABLE MEMORY FOR LONGEST PREFIX ADDRESS MATCHING	WASHBURN , JAMES G.
<u>09186562</u>	<u>6081440</u>	150	11/05/1998	TERNARY CONTENT ADDRESSABLE MEMORY (CAM) HAVING FAST INSERTION AND DELETION OF DATA VALUES	WASHBURN, JAMES G.
<u>08893914</u>	<u>5992436</u>	150	07/11/1997	MONITORING STEAM TRAPS USING RF SIGNALING	WASHBURN , JAMES F.
<u>07902251</u>	<u>5281832</u>	150	06/22/1992	BIDIRECTIONAL TWO- TERMINAL THYRISTOR	WASHBURN , JAMES R.
<u>07534431</u>	<u>5157779</u>	150	06/07/1990	USER EXTENSIBLE TESTING SYSTEM	WASHBURN , JAMES C.
<u>06396554</u>	<u>4529999</u>	150	07/09/1982	IMPROVED GATE CONTROLLED SWITCH	WASHBURN , JAMES R.
<u>06240735</u>	<u>4374604</u>	150	03/05/1981	CONTACT FOR AN ELECTRICAL CONNECTOR	WASHBURN , JAMES C.
<u>06185195</u>	<u>4318315</u>	150	09/08/1980	RATCHET WRENCH WITH RETURN SOCKET	WASHBURN , JAMES R.

Inventor Search Completed: No Records to Display.

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